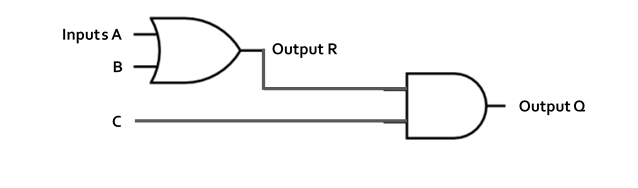
**Logic Retrieval Quiz**

**1**: Label each logic gate, the complete the truth tables for each logic gate.

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
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|  | |  | | |  |  | | |
| A | OUT | A | B | OUT |  | A | B | OUT |
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|  | |  |  |  |  |  |  |  |
|  | |  |  |  |  |  |  |  |

**2**: Complete the truth table for the following circuit.



|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **A** | **B** | **C** | **R** | **Q** |
|  |  |  |  |  |
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**3:** Create a ‘Logic Circuit Diagram’ for each of the given expressions.

|  |  |
| --- | --- |
| **Expression** | **Circuit** |
| P = NOT (A OR B) |  |
| P = (A AND B) OR C |  |
| P= NOT ((A AND B) AND C) |  |